



## U.S. **UTILITY** Patent Application

PATENT	NUM	BER	and
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10072734	02/05/2002	324	754	2858	KA	RLS	١,
**APPLICANT	S: Hembre	ee David;	Farnworth Wa	ırren; Ak	ram Salma	n; Wood Ala	an;
Doherty C : Kr	ivy Andrew;			282	29		
**CONTINUIN	G DATA VERIFIE	D:					
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	OIV OF 09/303,367						
WHICH IS A D	NV OF 08/797,719	02/11/199	97 PAT 6.060.	891			
** FOREIGN A	PPLICATIONS VI	ERIFIED:					
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oreign priority cla			s 🗆 no		ATTORI	NEY DOCKE	ET NO
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TITLE : Method	for testing semico	onductor w	vafers		USDEPT	DECOMMONDATA I	TM-PTO-436L(Rev. 1
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NOTICE OF ALL	OWANCE MAILED		CLAIMS ALLOWED				
	<del>'</del>	Assistant Examiner	Total Claims		rint Claim for .G		
ISSUE FEE			DRAWING				
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.	Print Fig.		
		Primary Examiner	<del></del>	L	<b>I</b>		
TERMINAL		PREPARED FOR ISSUE	Application Examiner				
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		FILED WITH: DISK	(CRF) (Attached in	n pocket on ri	CD-ROM ght inside flap)		